

## **BASYS board vs. NEXYS 2 board**

Most of the programs in *Learning By Example Using VHDL – Advanced Digital Design With a NEXYS 2™ FPGA Board* will run on the BASYS board. Of the 54 Examples in this book, the examples that will NOT run on the BASYS board include:

### **4. Memory**

Example 31 – External RAM

(No external RAM on BASYS board)

Example 32 – External Flash Memory

(No external Flash memory on BASYS board)

### **5. UART**

Example 33 – Transmit Module

(No UART on BASYS board)

Example 34 – Receive Module

### **6. VGA Controller**

Example 39 – External Video RAM

(No external RAM on BASYS board)

Example 40 – External Video Flash

(No external Flash memory on BASYS board)

### **8. Graphics**

Example 43 – Clearing the Screen

(Needs video RAM from Example 39)

Example 44 – Plotting a Dot

Example 45 – Plotting a Line

Example 46 – Plotting a Star

Example 47 – Plotting a Circle